

**Notice of References Cited**Application/Control No.  
09/803,593Applicant(s)/Patent Under  
Reexamination  
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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,872,481	02-1999	Sevic et al.	330/51
	B	US-3,755,693	08-1973	Lee, James Yat	326/78
	C	US-6,320,913	11-2001	Nakayama, Masahiko	375/297
	D	US-6,160,445	12-2000	Schweighofer, Peter	330/10
	E	US-5,387,876	02-1995	Sondermeyer, Jack C.	330/251
	F	US-4,348,644	09-1982	Kamiya, Shingo	330/297
	G	US-6,204,730	03-2001	Servaes, Jan	330/251
	H	US-4,706,039	11-1987	Dijkmans et al.	330/297
	I	US-6,411,136	06-2002	Nianxiong et al.	327/108
	J	US-6,567,491	05-2003	McCune et al.	375/247
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	D.A. Johns, D.Essig; Integrated Circuits for Data Transmission over Twisted-Pair Channels; IEEE 1996 Custom Integrated Circuits Conference; Pg. 2.1.1-2.1.8.
	V	H. Khorramabadi; A CMOS Line Driver with 80-dB Linearity for ISDN Applications; IEEE Journal of Solid State Circuits; Vol. 27, No. 4; April 1992; Pg. 539-544.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.